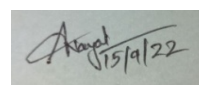


LESSON PLAN (Winter-2022)

Discipline: Electrical	Semester: 5th	Name of the Teaching Faculty: Amit Kumar Nayak
Subject: DE& MP (TH-3)	No of Days /per week class allotted: 5	Semester From date: 15.09.2022 To 22.12.2022 No of Weeks: 14
Week	Class Day	Theory Topics
1st		Verify truth tables of AND, OR, NOT, NOR, NAND, XOR, XNOR gates
2nd		Implement various gates by using universal properties of NAND & NOR gates and verify truth table
3rd		PUJA HOLIDAYS
4th		Implement half adder and Full adder using logic gates.
5th		Implement half subtractor and Full subtractor using logic gates.
6th		Implement a 4-bit Binary to Gray code converter.
7th		Implement a Single bit digital comparator.
8th		Study Multiplexer and demultiplexer
9th		Study of flip-flops. i) S-R flip flop ii) J-K flip flop iii) flip flop iv) T flip flop
10th		Study shift registers.
11th		General Programming using 8085A development board 1. a. 1'S Complement. b. 2'S Complement.
12th		2. a. Addition of 8-bit number. b. Subtraction of 8-bit number resulting 8/16 bit number
13th		4. a. Compare between two numbers. b. Find the largest in an Array
14th		5. Block Transfer.
15th		1. Traffic light control using 8255, 2. Generation of square wave using 8255



Signature of the Faculty